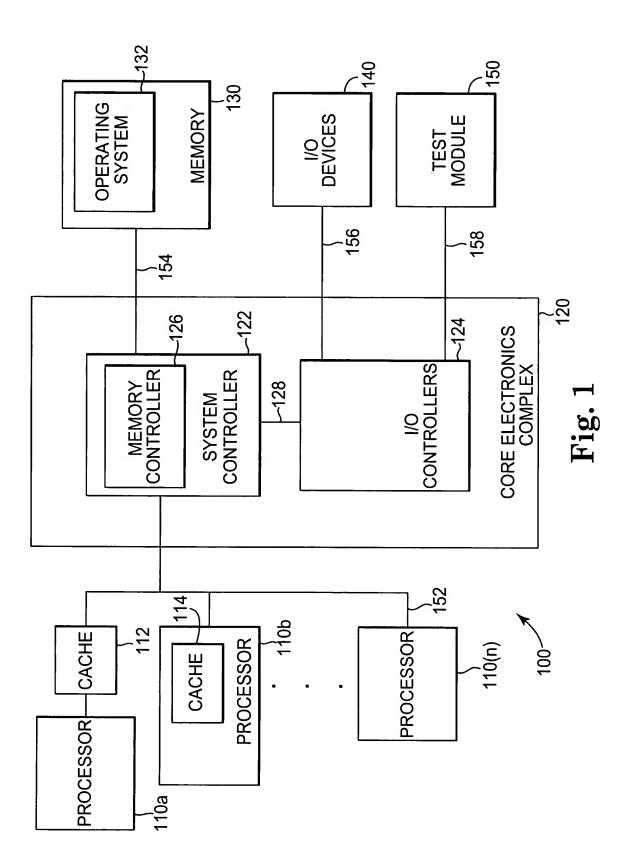
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Title: System and method for testing a component in

A COMPUTER SYSTEM USING VOLTAGE MARGINING

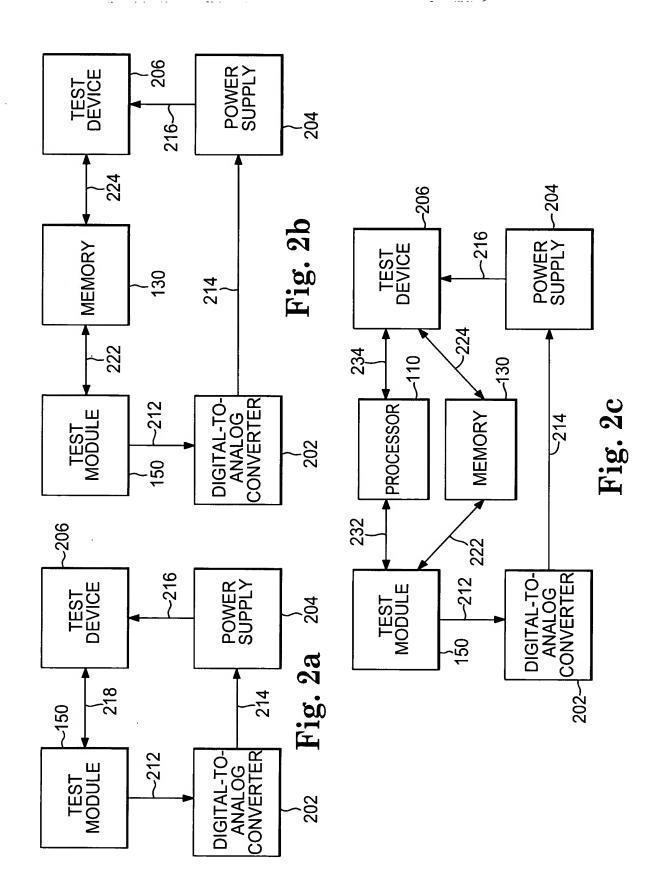
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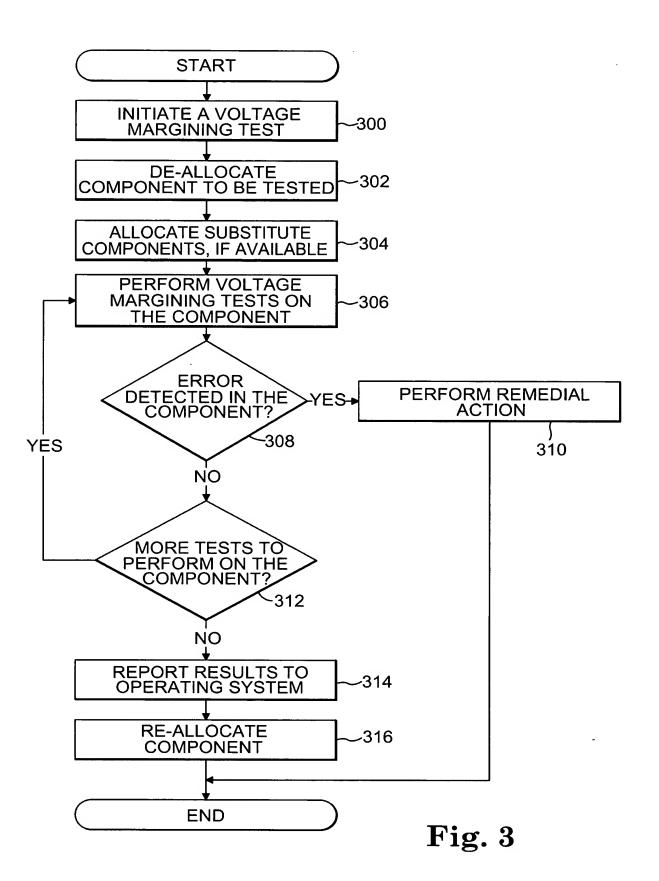
Applicant: Ken G. Pomaranski et al. HP Docket No.: 200209699-1

Title: system and method for testing a component in a computer system using voltage margining

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Title: System and method for testing a component in a computer system using voltage margining

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